



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Primary Applicant:

Robert Bristol et al.

§ Art Unit: 1756

Serial No.: 10/679,816

§ Examiner: Kathleen Duda

Filed: October 6, 2003

§ Atty Docket: ITL.1023US
P16710

For: Enhancing Photoresist
Performance Using
Electric Fields

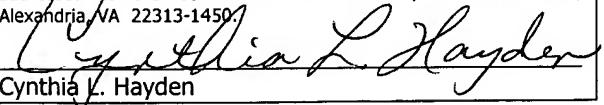
§ Assignee: Intel Corporation

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement mailed July 18, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: <u>August 1, 2006</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden